Introduction

Transductions

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Specification

Describe properties

Computation

Decide those properties





Many extensions: infinite words, finite and infinite trees, graphs, other logics ...



Famous application: Model-checking $A \models \phi$?



What about transductions ?

$$f: \Sigma^* \hookrightarrow \Sigma^*$$

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Append a #

 $abbab \mapsto abbab \#$

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Append a #Delete all b $abbab \mapsto abbab \#$

 $abbab \mapsto aa$

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Append a # $abbab \mapsto abbab#$ Delete all b $abbab \mapsto aa$

Squeeze all white space sequences ≥ 2 $ejcim_{19} \mapsto ejcim_{19}$

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Outline

- 1. automata for transductions
- 2. closure properties and decision problems
- 3. logics for transductions
- 4. a more expressive class of functions
- 5. recent results

Regular Functions

Automata models for transductions





$aabaa \mapsto aaaa$







 $aabaa \mapsto aaaa$ $aaba \mapsto$ undefined $dom(f_{del}) =$ 'even number of a' Introduction

Transducers

Logic

Regular Functions

Recent Results

Parity bit

$01101\mapsto \mathbf{1}01101, \mathbf{0}1111\mapsto \mathbf{0}01111$



Logic

Parity bit

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Logic

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Introduction Transducers Logic Regular Functions

Non-determinism and relations

In general, transducers define binary relations in $\Sigma^* \times \Sigma^*$



realizes $\{(u, v) \mid v \text{ is a subword of } u\}$

Formal Definition

Definition

A transducer is a tuple $T=(\Sigma,Q,I,F,\Delta)$ where:

- Σ is a finite alphabet
- \blacktriangleright Q is a finite set of states
- ▶ $I \subseteq Q$ are the initial states and $F \subseteq Q$ are the final states
- $\Delta \subseteq Q \times \Sigma \times \Sigma^* \times Q$ is the transition relation.

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Semantics

A <u>run</u> is a sequence of transitions

$$r = q_0 \xrightarrow{\sigma_1:v_1} q_1 \dots q_{n-1} \xrightarrow{\sigma_n:v_n} q_n \qquad \sigma_i \in \Sigma$$

Its input is $in(r) = \sigma_1 \dots \sigma_n$ and its output $out(r) = v_1 \dots v_n$. The (rational) relation defined by T is:

 $[\![T]\!] = \{(in(r), out(r)) \mid r \text{ is an accepting run}\}$

Closure Properties: Domain and Co-Domain

Given $R \subseteq \Sigma^* \times \Sigma^*$:

- $\blacktriangleright \ dom(R) = \{ \underline{u} \mid \exists (\underline{u}, v) \in R \}$
- $\blacktriangleright \ codom(R) = \{v \mid \exists (\mathbf{u}, v) \in R\}$

Closure Properties: Domain and Co-Domain

Given $R \subseteq \Sigma^* \times \Sigma^*$:

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Proposition

The domain and co-domain of a rational relation are regular.

Regular Functions

Recent Results

Closure Properties: Union

Proposition

Rational relations are closed under union.

1. show that $\{(a^n b^m, a^n) \mid n, m \ge 0\}$ is rational.

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3. are rational relations closed under intersection ? why ?

Proposition

Rational relations are not closed under intersection.

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Rational relations are not closed under intersection. What about complement ?

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Closure Properties: Composition

Def: $R_2 \circ R_1 = \{(u, w) \mid \exists (u, v) \in R_1, (v, w) \in R_2\}.$

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Proposition

Rational relations are closed under composition.
Let
$$T_1 = (\Sigma, Q_1, I_1, F_1, \Delta_1)$$
 and $T_2 = (\Sigma, Q_2, I_2, F_2, \Delta_2)$.
For all $u \in \Sigma^*$ and $p_2 \in Q_2$, let

$$\operatorname{Prod}_2(u, p_2) = \{(v, q_2) \in \Sigma^* \times Q_2 \mid p_2 \xrightarrow{u \mid v}_{T_2} q_2\}$$

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$$\operatorname{Prod}_2(u, p_2) = \{ (v, q_2) \in \Sigma^* \times Q_2 \mid p_2 \xrightarrow{u \mid v}_{T_2} q_2 \}$$

► The composition $\llbracket T_2 \rrbracket \circ \llbracket T_1 \rrbracket$ is realised by the transducer $T = (\Sigma, Q_1 \times Q_2, I_1 \times I_2, F_1 \times F_2, \Delta)$ where:

$$\Delta = \{ (p_1, p_2) \xrightarrow{\sigma | v} (q_1, q_2) \mid \exists p_1 \xrightarrow{\sigma | u}_{T_1} q_1 \land (v, q_2) \in \operatorname{Prod}_2(u, p_2) \}$$

Transducer vs Automata



Transducer vs Automata



• Consider r_1, r_2 two runs on a^3 . We have $(in(r_1), out(r_1)) = (in(r_2), out(r_2))$ but different in-out words:

 $(a,a)(a,a)(a,\epsilon) \neq (a,\epsilon)(a,a)(a,a)$

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• Consider r_1, r_2 two runs on a^3 . We have $(in(r_1), out(r_1)) = (in(r_2), out(r_2))$ but different in-out words:

$$(a,a)(a,a)(a,\epsilon) \neq (a,\epsilon)(a,a)(a,a)$$

► Transducers are **asynchronous**

▶ Make most transducer problems conceptually difficult (and even computationally).



Different classes of transductions



 $f_{\text{SWAP}} \ : \ u\sigma \to \sigma u \qquad \sigma \in \{a,b\}, u \in \{a,b\}^*$



Are the classes of sequential and rational functions decidable ? Class Membership Problems (for transductions) Given T a non-deterministic transducer: Functionality decide if [T] is a function, Determinizability decide if T is equivalent to some input-deterministic transducer.

PARITY

JSWAP '

SUBWORD

Idel

Some application of the functionality problem

Testing unambiguity of NFA.

Another Fundamental Problem: Equivalence

Def Given two transducers T_1, T_2 , does $\llbracket T_1 \rrbracket = \llbracket T_2 \rrbracket$ hold?

Case of functional transducers Equivalence reduces to functionality:

- 1. test whether $dom(T_1) = dom(T_2)$
- 2. test whether $T_1 \uplus T_2$ is functional.

Functionality problem: Results

- **Lem (Schützenberger)** Non-functionality is witnessed by runs r_1, r_2 such that
 - (1) r_1, r_2 are over the same input
 - (2) r_1, r_2 produce different outputs
 - (3) r_1, r_2 have polynomial length

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Coro Functionality is decidable in PSPACE.

Proof of the Lemma

Assume $(u, v), (u, w) \in R$ where $v \neq w$, given by runs r_1, r_2 resp. If u is long enough:



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Proof (Ced)

 $(v_1v_4 = w_1w_4 \wedge v_1v_2v_4 = w_1w_2w_4 \wedge v_1v_3v_4 = w_1w_3w_4) \implies v_1v_2v_3v_4 = w_1w_2w_3w_4$

1. Wlog, assume that $v_1 = \epsilon$. If not, assume v_1 prefix of w_1 , i.e. $w_1 = v_1 w'_1$ and eliminate v_1 from all rhs (the case w_1 prefix of v_1 is symmetric). So, we want

 $(v_4 = w_1 w_4 \land v_2 v_4 = w_1 w_2 w_4 \land v_3 v_4 = w_1 w_3 w_4) \implies v_2 v_3 v_4 = w_1 w_2 w_3 w_4$

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2. In $v_2v_4 = w_1w_2w_4$, replace v_4 by w_1w_4 , then we get $v_2w_1w_4 = w_1w_2w_4$. Similarly, one gets $v_3w_1w_4 = w_1w_3w_4$. Simplify by w_4 and we get:

$$v_2 w_1 = w_1 w_2$$
 (1) $v_3 w_1 = w_1 w_3$ (2)

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3. Finally:

$$v_2v_3v_4 = v_2v_3w_1w_4 \quad \text{by } v_4 = w_1w_4$$
$$= v_2w_1w_3w_4 \quad \text{by (2)}$$
$$= w_1w_2w_3w_4 \quad \text{by (1)}$$

Functionality Problem in PTIME

Thm (Gurari, Ibarra, 83). Functionality is decidable in PTIME.

- ▶ reversal-bounded counter machines
- ▶ emptiness in PTIME if fixed number of counters
- later shown with a direct proof by Carton,Beal,Prieur,Sakarovitch (Squaring Transducers)

Some Definitions

Given a transducer $T = (\Sigma, Q, I, F, \Delta)$,

• $q \in Q$ is <u>co-accessible</u> by u if $q \xrightarrow{u|v}{\to} q_f \in F$ for some v

▶ CoAcc =
$$\{(p,q) \in Q^2 \mid p, q \text{ co-accessible by some } u\}$$

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- ▶ Let $u, v \in \Sigma^*$, $u \wedge v$ is the longest common prefix of u and v
- ▶ delay(u, v) = (u', v') where $u = (u \land v)u'$ and $v = (u \land v)v'$

Squaring Transducers Carton, Beal, Prieur, Sakarovitch, 01

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- Let $u, v \in \Sigma^*$, $u \wedge v$ is the longest common prefix of u and v
- delay(u, v) = (u', v') where $u = (u \wedge v)u'$ and $v = (u \wedge v)v'$

Lemma (The delay is compositional) $delay(u_1u_2, v_1v_2) = delay(delay(u_1, u_2).(v_1, v_2)).$





Recent Results

Squaring Transducers

State delays

$$\mathsf{delays}(p,q) = \{\mathsf{delay}(v,w) \mid \exists u \in \Sigma^* \exists p_0 \xrightarrow{u|v} p \exists q_0 \xrightarrow{u|w} q\}$$

Squaring Transducers

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First observation T is functional iff for all states $(p_f, q_f) \in F^2 \cap Acc$, $delays(p_f, q_f) = \{(\epsilon, \epsilon)\}$

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First observation T is functional iff for all states $(p_f, q_f) \in F^2 \cap Acc$, $delays(p_f, q_f) = \{(\epsilon, \epsilon)\}$

Second observation Let $(p,q) \in CoAcc$. If |delays(p,q)| > 1 then T is not functional.

Introduction Transducers Logic Regular Functions

Recent Results

Proof of the Second Observation

Second observation Let $(p,q) \in CoAcc$. If |delays(p,q)| > 1 then T is not functional.

Proof of the second observation



Assume

 $\begin{array}{l} (\alpha_1,\beta_1):=\operatorname{delay}(u_1,v_1) \\ \neq \\ (\alpha_1',\beta_1'):=\operatorname{delay}(u_1',v_1'). \end{array}$

By contradiction, assume that $u_1u_2 = v_1v_2$ and $u'_1u_2 = v'_1v_2$. Then $\alpha_1u_2 = \beta_1v_2$ and $\alpha'_1u_2 = \beta'_1v_2$. As (α_1, β_1) and (α'_1, β'_1) are delays, the following cases may arise:

- 1. α_1 and β_1 start with distinct letters. Impossible.
- 2. α'_1 and β'_1 start with distinct letters. Impossible.

3. $\alpha_1 = \alpha'_1 = \epsilon$: then $u_2 = \beta_1 v_2 = \beta'_1 v_2$, hence $\beta_1 = \beta'_1$, impossible.

4. $\alpha_1 = \beta'_1 = \epsilon$, then $u_2 = \beta_1 v_2$ and $v_2 = \alpha'_1 u_2$. Then $u_2 = \beta_1 \alpha'_1 u_2$, hence $\beta_1 = \alpha_1 = \alpha'_1 = \beta'_1 = \epsilon$, impossible.

5. cases $\beta_1 = \beta'_1 = \epsilon$ and $\beta_1 = \alpha'_1 = \epsilon$ are symmetrical.

Squaring Transducers: Algorithm for functionality

- 1. compute CoAcc (quadratic time)
- 2. Visited = { $(p_0, q_0, (\epsilon, \epsilon)) \mid (p_0, q_0) \in \text{CoAcc} \cap I^2$ }
- 3. Waiting = Visited
- 4. While (Waiting $\neq \emptyset$)
- 5. Remove some $(p, q, d) \in Waiting$
- 6. For all $p \xrightarrow{\sigma:v} p'$, $q \xrightarrow{\sigma:w} q'$ s.t. $(p',q') \in CoAcc$ do:

7.
$$d' = \operatorname{delay}(d.(v, w))$$

8. **if**
$$(p', q', d') \notin$$
 Visited:

- 9. **if** $\exists (p', q', d'') \in \text{Visited s.t. } d' \neq d'' \text{ return NO}$
- 10. **if** $(p',q') \in F^2$ and $d' \neq (\epsilon,\epsilon)$ **return** NO
- 11. add (p', q', d') to Waiting and to Visited

12. return YES

Logic	Regular Functions
	Logic

Invariant

Lemma

For all $(p, q, d) \in V$ is ited, there exist $p_0, q_0 \in I$, $u, v, w \in \Sigma^*$ such that:

1. $p_0 \xrightarrow{u:v} p$ 2. $q_0 \xrightarrow{u:w} q$ 3. d = delay(v, w)4. $(p,q) \in CoAcc$

Correctness of the algorithm

1. if it returns NO, then by the Invariant and the two observations, T is not functional

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- conversely, if it returns YES, then we show that in the end, we have (*)

Visited $\supseteq \{(p, q, \mathsf{delay}(v, w)) \mid \exists p_0 \xrightarrow{u:v} p, q_0 \xrightarrow{u:w} q, (p, q) \in \mathrm{CoAcc}\}$

and in particular, Visited contains all such $(p,q, \operatorname{\mathsf{delay}}(v,w))$ such that $(p,q) \in F^2 \cap \operatorname{Acc.}$

Correctness of the algorithm

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and in particular, Visited contains all such $(p,q, \operatorname{\mathsf{delay}}(v,w))$ such that $(p,q) \in F^2 \cap \operatorname{Acc.}$ If T is not functional, by Obs1 there exists (p,q,d) such that $(p,q) \in F^2 \cap \operatorname{Acc}$ and $d \neq (\epsilon, \epsilon)$, hence the test at line 10 eventually fails. Contradiction.

To show \star , use induction on |u| and delay compositionality.

Summary of yesterday's talk



Given T a non-deterministic transducer:

Functionality decide if $\llbracket T \rrbracket$ is a function,

Determinizability decide if T is equivalent to some input-deterministic transducer.

 \rightarrow We have seen that Functionality can be decided in PTime.

Equivalence Problem: Results

For functional transducers T_1, T_2

- ▶ PSPACE-C (hardness by automata equivalence)
- PTIME if $dom(T_1) = dom(T_2)$ is known.

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In general

► Undecidable (Griffith 68), even if one alphabet is unary (Ibarra 78)
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In general

- ► Undecidable (Griffith 68), even if one alphabet is unary (Ibarra 78)
- ▶ Decidable for finite-valued transducers (Culik Karhumäki 86)¹.

Summary-Transducers

Expressiveness:

input-deterministic	functional	non-deterministic
f _{del} <	f _{swap}	R _{subword}
??:	? PTIME	

Summary – Transducers

Expressiveness:



Equivalence: $(dom(T_1) = dom(T_2) \text{ is known})$

input-deterministic	functional	non-deterministic
PTime	PTime	undec

Determinizability

Def Given a transducer T, does there exist an input-deterministic transducer T' such that [T] = [T']?

Remark: we now assume that T is:

- ▶ functional (otherwise the answer is NO)
- ▶ trim (can be achieved in PTime)

Determinizability

Def Given a transducer T, does there exist an input-deterministic transducer T' such that [T] = [T']?

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Thm (Choffrut77, Weber,Klemm,95). Determinizability is decidable in PTIME.

- ▶ equivalent input-deterministic transducer of exp. size
- characterization based on a pattern of the transducer (twinning property) due to (Choffrut77)
- ▶ PTIME membership due to (Weber,Klemm,95)

 $_{-}$ = white space



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Is non-determinism needed ?







Is non-determinism needed ? No.



- extend automata subset construction with outputs
- output the longest common prefix



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Can we always get an equivalent deterministic FT ?

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▶ not in general: input-deterministic transducers are less expressive than functional ones

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Semantics

$$\llbracket T \rrbracket : \left\{ \begin{array}{l} a^n b \mapsto b^{n+1} \\ a^n c \mapsto c^{n+1} \end{array} \right.$$

functional but not determinizable

























How to guarantee termination of subset construction?

Reminder: delay(u, v) = (u', v') such that $u = \ell u', v = \ell v'$ and $\ell = u \wedge v$.

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Reminder: delay(u, v) = (u', v') such that $u = \ell u', v = \ell v'$ and $\ell = u \wedge v$. We say that T satisfies the Twinning Property iff for all

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it is the case that $delay(v_1, w_1) = delay(v_1v_2, w_1w_2)$.

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Reminder: delay(u, v) = (u', v') such that $u = \ell u', v = \ell v'$ and $\ell = u \wedge v$. We say that T satisfies the Twinning Property iff for all

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it is the case that $delay(v_1, w_1) = delay(v_1v_2, w_1w_2)$.

Lemma[Characterization] T is determinizable iff it satisfies the Twinning Property.

Proof of the characterization $(T \models TP \Rightarrow T \text{ det.})$

(n: number of states, M: maximal length of output word)

Lemma If T satisfies the Twinning Property, then for all runs $p_0 \xrightarrow{u:v} p$ and $q_0 \xrightarrow{u:w} q$, we have $|\mathsf{delay}(v,w)| \leq 2n^2 M$.

Proof of the characterization $(T \models TP \Rightarrow T \text{ det.})$

(*n*: number of states, M: maximal length of output word) Lemma If T satisfies the Twinning Property, then for all runs $p_0 \xrightarrow{u:v} p$ and $q_0 \xrightarrow{u:w} q$, we have $|\mathsf{delay}(v, w)| \leq 2n^2 M$.

Proof: We proceed by contradiction, and consider a counter-example of minimal length, with input word u. Two cases:

- If $|u| \le n^2$, then $|\mathsf{delay}(v, w)| \le |v| + |w| \le 2n^2 M$.
- ► If |u| > n², then there is a loop. By the twinning property, and the compositionality of delay, we obtain a shorter counter-example. Contradiction.

Proof of the characterization $(T \models TP \Rightarrow T \text{ det.})$

(*n*: number of states, M: maximal length of output word) Lemma If T satisfies the Twinning Property, then for all runs $p_0 \xrightarrow{u:v} p$ and $q_0 \xrightarrow{u:w} q$, we have $|\mathsf{delay}(v, w)| \leq 2n^2 M$.

Proof: We proceed by contradiction, and consider a counter-example of minimal length, with input word u. Two cases:

- If $|u| \le n^2$, then $|\mathsf{delay}(v, w)| \le |v| + |w| \le 2n^2 M$.
- ▶ If |u| > n², then there is a loop. By the twinning property, and the compositionality of delay, we obtain a shorter counter-example. Contradiction.

We have: $T \models$ Twinning Property \Rightarrow Subset constr. terminates

 $\Rightarrow T$ determinizable

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Any equivalent input-deterministic transducer should store them, impossible with finitely many states.
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Consider an input-deterministic transducer D s.t. $[\![T]\!] = [\![D]\!]$ and an instance of the Twinning Property:



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We obtain : (for some output words x_1, x_2, x_3, y_3 in D)

$$\begin{aligned} \forall \ell \geq 0, \quad & [\![T]\!](u_1u_2^j \ u_2^{\ell i} \ u_3) \quad = v_1v_2^j \ v_2^{\ell i}v_3 \quad = x_1x_2^\ell x_3 \\ & [\![T]\!](u_1u_2^j \ u_2^{\ell i} \ u_4) \quad = w_1w_2^j \ w_2^{\ell i}w_3 \quad = x_1x_2^\ell y_3 \end{aligned}$$

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Thus $v_1 v_2^{\omega} = x_1 x_2^{\omega} = w_1 w_2^{\omega}$ and $|v_2| = |w_2|$. This entails $delay(v_1, w_1) = delay(v_1 v_2, w_1 w_2)$.

 $\label{eq:lemma} \begin{array}{l} \textbf{Lemma} [\text{Characterization}] \ T \ \text{is determinizable iff it satisfies the} \\ \text{Twinning Property.} \end{array}$

For all situations



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Lemma[Characterization] T is determinizable iff it satisfies the Twinning Property.

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Claim: T <u>violates</u> the TP iff there exists situation as above such that:

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$$|v_2| \neq |w_2|$$
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To decide 1.:

consider a weighted graph with vertices (p,q) such that $(p,q) \xrightarrow{n} (p',q')$ iff $\exists \sigma$, $p \xrightarrow{\sigma:w} p', q \xrightarrow{\sigma:w} q'$ and n = |v| - |w| \rightsquigarrow verify that every cycle has weight 0

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All together: decidable in PTime (and even in NLogSpace)

Summary-Transducers

Expressiveness:

input-deterministic	functional	non-deterministic
f _{del} <	fswap <	R _{subword}
PTI	ME PI	TIME

Summary – Transducers

Expressiveness:



Equivalence: $(dom(T_1) = dom(T_2)$ is known)

input-deterministic	functional	non-deterministic
PTime	PTime	undec

Regular Functions

Recent Results

Logics for transductions

Over some finite alphabet Σ :

 $\varphi \ ::= \ \varphi \wedge \varphi \mid \neg \varphi \mid \exists x \varphi \mid \exists X \varphi \mid x \in X \mid \sigma(x) \mid S(x,y) \qquad \sigma \in \Sigma$

Over finite words, (set) variables interpreted by (sets of) positions.

Notation: \leq is the transitive closure of S

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Some examples

- first position is an $a: \exists x \ a(x) \land \forall y(x \leq y)$
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Büchi-Elgot-Trakhenbrot

 $L \subseteq \Sigma^*$ is MSO-definable iff it is recognisable by some FA.

Examples of MSO formulae

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• Set X is the set of even positions: $\phi_{even}(X) =$

 $\phi(x)$: MSO formula with one free FO variable x $w \in \Sigma^*, i \in \{1, \dots, |w|\}$ Notation: $w, i \models \phi(x)$: $\phi(x)$ evaluates to True at position i in w

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Example (Delete a's)

$$\phi_{b}(x) \equiv \frac{b}{b}(x)$$

$$\phi_{\epsilon}(x) \equiv \frac{a}{a}(x)$$

Realizes the function $f: u \in \{a, b\}^* \mapsto b^{|u|_b}$

Example (Append #)

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Example (Add a parity bit)

► replace label σ of x by 1σ if x is the first position and odd number of 1

► replace label σ of x by 0σ if x is the first position and even number of 1

Extension to transductions Example (Add a parity bit)

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Büchi Theorem for Rational Transductions

Def $f: \Sigma^* \hookrightarrow \Sigma^*$ is MSO-definable if it can be "described" by a finite set of formulas $\phi_{v_1}(x), \ldots, \phi_{v_k}(x)$ $(v_1, \ldots, v_k \in \Sigma^*)$.

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What about mirror ? $ejcim \mapsto micje$

Replace label of position x by σ if last - x is labeled σ . Not MSO-definable.

W.l.o.g., we assume T unambiguous.

For each transition t = (p, a, v, q) of T, we define the language $L_t \subseteq (\Sigma \times \{0, 1\})^*$ such that:

 $\overline{w} \in L_t \iff \exists \operatorname{run} q_0 \xrightarrow{u|v} q_f \text{ s.t. } \pi_2(\overline{w})[i] = 1 \text{ iff } t \text{ used at position } i$

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 L_t is regular, recognized by A_t , obtained as follows:

▶
$$p \xrightarrow{(a,1)} q$$

▶ $p' \xrightarrow{(b,0)} q'$ for each transition $t' = (p', b, v', q') \neq t$

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We let
$$\phi_v(x) = \bigvee_{t=(p,a,v,q)} \phi_t(x).$$

Proof: from MSO to transducers

For each $\phi_v(x)$, build an automaton A_v that recognizes words $\overline{w} \in (\Sigma \times \{0,1\})^*$ such that $\pi_2(\overline{w})[i] = 1$ iff $w, i \models \phi_v(x)$.

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Recent Results

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Claim: As $\phi_{v_1}, \ldots, \phi_{v_k}$ define a function f, for each word $w \in dom(f)$, and for each position $i \in \{1, \ldots, |w|\}$, there is exactly one j such that $w, i \models \phi_{v_j}$.

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Consider the automaton $A = \prod_{j=1}^{k} A_{v_j}$, synchronised on Σ . Transform it into a transducer by outputting v_j if transition (a, 1) is used in A_{v_j} .

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This construction is correct thanks to previous claim.

The class of regular functions















































Some important results on two-way transducers

Over (functional) transductions:

▶ equivalence is decidable in PSPACE

(Gurari 82) (Culik, Karhumäki,87)

 $^{^{2}\}Sigma^{*}a\Sigma^{*}$ is not definable by any one-way reversible automaton

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▶ and to many other models ...

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Transducers with registers



deterministic one-way

• equivalent to 2FT if linear updates

(Alur, Cerny, 10)

decidable equivalence problem

(F., R.,17) (Benedikt et. al., 17)

"interpreting the output structure in the input structure"



"interpreting the output structure in the input structure"

"interpreting the output structure in the input structure" $% \mathcal{A}^{(n)}$

 output predicates defined by MSO formulas interpreted over the input structure

▶ input structure can be copied a fixed number of times: $u \mapsto uu$, or $u \mapsto u$.mirror(u).

Transducers Logic Regular Functions Recent Results Other example : $u \mapsto u.\operatorname{mirror}(u)$ (e) s (e) $\left(s \right)$ t (r) (s) ${\rm copy}\ 1$ (d) (r) (e) $\left(s \right)$ (s) (e) (d) $\operatorname{copy}\,2$ t) s)







Formulas

copy 1: $\phi_S^1(x,y) \equiv S(x,y)$





Formulas





Formulas

copy 1: $\phi_S^1(x,y) \equiv S(x,y)$ copy 2: $\phi_S^2(x,y) \equiv S(y,x)$ copy 1 to copy 2: $\phi_S^{1\to 2}(x,y) \equiv x = y \wedge last(x)$



Formulas

 $\begin{array}{rcl} \operatorname{copy} \mathbf{1} & \phi_S^1(x,y) & \equiv & S(x,y) \\ & \operatorname{copy} \mathbf{2} & \phi_S^2(x,y) & \equiv & S(y,x) \\ \end{array}$ $\begin{array}{rcl} \operatorname{copy} \mathbf{1} \text{ to copy} \ \mathbf{2} & \phi_S^{1 \to 2}(x,y) & \equiv & x = y \wedge last(x) \\ \end{array}$ $\begin{array}{rcl} \operatorname{copy} \ \mathbf{2} \text{ to copy} \ \mathbf{1} & \phi_S^{2 \to 1}(x,y) & \equiv & \bot \end{array}$



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Recent Results

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Büchi Theorem for Regular Transductions Let $f: \Sigma^* \hookrightarrow \Sigma^*$.

Theorem (Engelfriet, Hoogeboom, 01)

The following are equivalent:

- $1. \ f \ is \ definable \ by \ a \ deterministic \ two-way \ transducer$
- $2. \ f \ is \ MSO-definable.$

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Proof ideas: MSO-transducers are 2-way transducers with MSO jumps $\phi^{c \to c'}_S(x,y)$

- turn jumps into walks
- hold enough information to decide MSO-formulas locally: states = MSO-types

 $f = \hat{f} \circ f_{types}$ (use composition closure of 2-way trans)

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Summary – Expressiveness

	input-deterministic	functional	non-deterministic
1-way (rational)	<	< <	
2-way (regular)	=	= <	

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Logic

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Summary – Expressiveness

	input-deterministic	functional	non-deterministic
1-way (rational)	· PTn	ME \uparrow PT DEC	Time 1 Undec
2-way (regular)		← PS	Pace

(F., Gauwin, R., Servais, 13)

(Baschenis, Gauwin, Muscholl, Puppis, 17)

Summary – Equivalence problem

 $dom(T_1) = dom(T_2)$ is known.

	input-deterministic	functional	non-deterministic
1-way (rational)	PTime	PTime	undec
2-way (regular)	PSPace	PSPace	undec

Some other (recent) results

- ▶ FO-transducers
 - equivalent to <u>aperiodic</u> transducers with registers (F., Krishna, Trivedi, 14)
 - ▶ and to aperiodic 2-way transducers (Dartois, Jecker, R., 16)

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- regular function expressions
 - iterated sum $f^*(u) = f(u_1)f(u_2)\dots f(u_n)$ for $u = u_1\dots u_n$
 - chain sum $f^c(u) = f(u_1u_2)f(u_2u_3)\dots f(u_{n-1}u_n)$
 - ▶ introduced by Alur, Freilich, Raghothaman in 14
 - ▶ direct construction from 2FT by Baudru, R. in 18
 - extended to infinite words by Dave, Gastin, Krishna in 18
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- ▶ an expressive decidable logic tailored to (non-functional) transductions Dartois, F., Lhote, 18

Definability Problems

Definition \mathcal{F} : logical fragment of MSOT (e.g. FOT) Input: T an MSOT Output: Is [T] FO-definable ?

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Results

- Decidable for "rational" MSOT (=rational functions)
 F., Gauwin, Lhote, 16
- ▶ Open for MSOT

Register Minimization Problems

Rational functions = Register transd. with updates X := Yu

For all situations like

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Theorem (Daviaud, R., Talbot, 16)

A transducer T can be expressed using k registers iff it satisfies the Twinning Property of order k.



there are two runs $0 \le i < j \le k$ s.t. for every loop ℓ , we have $delay(w_{l,i} \dots w_{\ell,i}, w_{l,j} \dots w_{\ell,j}) = delay(w_{l,i} \dots w_{\ell,i} w'_{\ell,j}, w_{l,j} \dots w_{\ell,j} w'_{\ell,j})$

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Other results:

- ▶ multi-sequential transducers Daviaud, Jecker, R., Villevalois, 17
- concatenation-free non-det reg. transducers Baschenis, Gauwin, Muscholl, Puppis, 16
- ► concatenation-free det. reg transducers R., Villevalois, 19

Origin semantics (Bojanczyk, 14)



Origin semantics $[T]_o$ inherent to most transducer models T !

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- ▶ decidable FO-definability of MSOT with origin
- ▶ algorithmic problems modulo origin $(\llbracket T_1 \rrbracket_o = \llbracket T_2 \rrbracket_o)$
- extended to "similar" origins through resynchronisers (F., Maneth, R., Talbot, 15) (F., Jecker, Löding, Winter, 16), (Bose, Muscholl, Penelle, Puppis, 18)

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- ▶ study of rational relation subclasses by control languages $REL(C), C \subseteq \{in, out\}^*$ (Descotte, Figueira, Libkin, Puppis)





transduction \approx set of origin graphs



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 $\mathrm{MSO}[\underline{\leq_{in}}, \underline{\leq_{out}}, o]$



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Results

- $T \models \phi$ decidable for 2-way transducers
- undecidable satisfiability
- decidable fragment with regular synthesis
- correspondence with data words

▶ machine-independent characterisations

Cadilhac, Krebs, Ludwig, Paperman, 15

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- uniformisation problems (Ismaël's Jecker and Sarah Winter's PhD thesis). E.g. given R rational, is there f sequential such that
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- data word transducers Léo Exibard's PhD thesis
- ▶ other structures: infinite strings, nested words, trees, graphs, data words ...

A Few Applications

- ▶ language and speech processing (M. Mohri)
- regular model-checking
- ▶ text analysis, document transformation
- reactive synthesis
- ▶ **Tools**: OpenFST, Vaucanson, DreX (Alur, d'Antoni, Raghothaman)
- ▶ line of works on symbolic transducers (d'Antoni, Veanes ...)

Introduction

Thanks!

Thanks for your attention!

Announcements

RP'19, September 11-13, Brussels

- ▶ conference on reachability problems
- ▶ talks with submitted papers or w/o
- ▶ best papers invited for a journal issue
- deadline in June
- ▶ invited speakers: Henzinger, Protasov, Lasota, Sriram S., Raskin.

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- 1 PostDoc position at ULB
 - ▶ transducer and synthesis problems
 - ▶ up to 2 years
 - ▶ flexible starting date