## Metric Interval Temporal Logic Revisited

MOVE seminar

Benjamin Monmege (LIF, Aix-Marseille Université)

Based on joint works with Thomas Brihaye, Hsi-Ming Ho, Morgane Estiévenart (UMONS), Gilles Geeraerts (ULB), and Nathalie Sznajder (LIP6)

30/03/2017





Events (*MoveUp*, *MoveDown*, *OpenDoor*...)



- Events (MoveUp, MoveDown, OpenDoor...)
- States (at which floor, opened/closed...)



- Events (MoveUp, MoveDown, OpenDoor...)
- States (at which floor, opened/closed...)
- Timings (operation time, latency...)



- Events (MoveUp, MoveDown, OpenDoor...)
- ▶ States (at which floor, opened/closed...)
- Timings (operation time, latency...)





- Events (MoveUp, MoveDown, OpenDoor...)
- ▶ States (at which floor, opened/closed...)
- Timings (operation time, latency...)



This is a timed automaton.

## The two semantics

What we consider as a *behaviour* of the system?



## The two semantics

What we consider as a *behaviour* of the system?



► Pointwise (event-based) view: timed word

(*MoveUp*, 1)(*Arrive*, 6)...

## The two semantics

What we consider as a *behaviour* of the system?



> Pointwise (event-based) view: timed word

(*MoveUp*, 1)(*Arrive*, 6)...

**Continuous** (state-based) view: signal from  $\mathbb{R}_{\geq 0}$  to states

	0F	0_TO_1					1F	
1		1	1	1	1	1	1	1
0	1	L	2	3	4	5	6	7

 $\varphi ::= \top \mid a \mid \neg \varphi \mid \varphi \land \varphi \mid \varphi \mathsf{U}_{I} \varphi$ with  $a \in \Sigma$ ,  $I \subseteq [0, \infty)$  with bounds in  $\mathbb{N} \cup \{+\infty\}$ .

 $\varphi ::= \top \mid \mathbf{a} \mid \neg \varphi \mid \varphi \land \varphi \mid \varphi \mathsf{U}_{\mathsf{I}} \varphi$ 

with  $a \in \Sigma$ ,  $I \subseteq [0, \infty)$  with bounds in  $\mathbb{N} \cup \{+\infty\}$ .

In the pointwise semantics:



 $\varphi ::= \top \mid \mathbf{a} \mid \neg \varphi \mid \varphi \land \varphi \mid \varphi \mathsf{U}_{\mathsf{I}} \varphi$ 

with  $a \in \Sigma$ ,  $I \subseteq [0, \infty)$  with bounds in  $\mathbb{N} \cup \{+\infty\}$ .

In the pointwise semantics:



'There is a *MoveUp* followed by an *Arrive* after 5 t.u.'

 $\varphi ::= \top \mid \mathbf{a} \mid \neg \varphi \mid \varphi \land \varphi \mid \varphi \mathsf{U}_{\mathsf{I}} \varphi$ 

with  $a \in \Sigma$ ,  $I \subseteq [0, \infty)$  with bounds in  $\mathbb{N} \cup \{+\infty\}$ .

In the pointwise semantics:



'There is a *MoveUp* followed by an *Arrive* after 5 t.u.'

 $(MoveUp \land \Diamond_{[5,5]}Arrive)$ 

## What do we want to do?

### 1. Satisfiability of an MTL formula

check whether a specification is consistent

## What do we want to do?

- 1. Satisfiability of an MTL formula
  - check whether a specification is consistent
- 2. Model-check a timed model against an MTL formula
  - verification of the system

## What do we want to do?

- 1. Satisfiability of an MTL formula
  - check whether a specification is consistent
- 2. Model-check a timed model against an MTL formula
  - verification of the system
- 3. Synthesise a valid system from an MTL specification, under certain restrictions on the environment
  - reactive synthesis task

# Part 1: satisfiability and model-checking

Based on a joint work with Thomas Brihaye (UMONS), Gilles Geeraerts (ULB), and Hsi-Ming Ho (UMONS)



### Submitted at CAV 2017 @ Heidelberg

Theorem: [Alur and Dill, 1994]

Timed automata are not closed under complementation.

Theorem: [Alur and Dill, 1994]

Timed automata are not closed under complementation.

Theorem: [Alur and Dill, 1994]

Universality and language inclusion are undecidable for timed automata.

Theorem: [Alur and Dill, 1994]

Timed automata are not closed under complementation.

#### Theorem: [Alur and Dill, 1994]

Universality and language inclusion are undecidable for timed automata.

Theorem: [Alur and Dill, 1994, Ouaknine and Worrell, 2006]

Satisfiability and model checking for MTL are undecidable (over infinite words).

Theorem: [Alur and Dill, 1994]

Timed automata are not closed under complementation.

Theorem: [Alur and Dill, 1994]

Universality and language inclusion are undecidable for timed automata.

Theorem: [Alur and Dill, 1994, Ouaknine and Worrell, 2006]

Satisfiability and model checking for MTL are undecidable (over infinite words).

Can we find a fully decidable subclass?

- ► TPTL [Alur and Henzinger, 1989]
  - $\Diamond x. (MoveUp \land \Diamond y. (Arrive \land y = x + 5))$

- ▶ TPTL [Alur and Henzinger, 1989]
  - $\Diamond x. (MoveUp \land \Diamond y. (Arrive \land y = x + 5))$
- MTL [Koymans, 1990]
  - $(MoveUp \land \Diamond_{[5,5]}Arrive)$

- ▶ TPTL [Alur and Henzinger, 1989]
  - $\Diamond x. (MoveUp \land \Diamond y. (Arrive \land y = x + 5))$
- MTL [Koymans, 1990]
  - $(MoveUp \land \Diamond_{[5,5]}Arrive)$
- ▶ TA [Alur and Dill, 1994]

- TPTL [Alur and Henzinger, 1989]
  - $\Diamond x. (MoveUp \land \Diamond y. (Arrive \land y = x + 5))$
- MTL [Koymans, 1990]
  - $(MoveUp \land \Diamond_{[5,5]}Arrive)$
- ► TA [Alur and Dill, 1994]
- TCTL [Alur, Courcoubetis, and Dill, 1990]
  - $A\Diamond(MoveUp \land A\Diamond_{[5,5]}Arrive)$

- ► TPTL [Alur and Henzinger, 1989]
  - $\Diamond x. (MoveUp \land \Diamond y. (Arrive \land y = x + 5))$
- MTL [Koymans, 1990]
  - $(MoveUp \land \Diamond_{[5,5]}Arrive)$
- ► TA [Alur and Dill, 1994]
- TCTL [Alur, Courcoubetis, and Dill, 1990]
  - $A\Diamond(MoveUp \land A\Diamond_{[5,5]}Arrive)$
- MITL [Alur, Feder, and Henzinger, 1996]
  - Same as MTL except that the bounding interval / must be non-singular

- ► TPTL [Alur and Henzinger, 1989]
  - $\Diamond x. (MoveUp \land \Diamond y. (Arrive \land y = x + 5))$
- MTL [Koymans, 1990]
  - $(MoveUp \land \Diamond_{[5,5]}Arrive)$
- ► TA [Alur and Dill, 1994]
- TCTL [Alur, Courcoubetis, and Dill, 1990]
  - $A\Diamond(MoveUp \land A\Diamond_{[5,5]}Arrive)$
- MITL [Alur, Feder, and Henzinger, 1996]
  - Same as MTL except that the bounding interval / must be non-singular
  - $(MoveUp \land \Diamond_{[4,6]}Arrive)$

- TPTL [Alur and Henzinger, 1989]
  - $\Diamond x. (MoveUp \land \Diamond y. (Arrive \land y = x + 5))$
- MTL [Koymans, 1990]
  - $(MoveUp \land \Diamond_{[5,5]}Arrive)$
- ► TA [Alur and Dill, 1994]
- TCTL [Alur, Courcoubetis, and Dill, 1990]
  - $A\Diamond(MoveUp \land A\Diamond_{[5,5]}Arrive)$
- MITL [Alur, Feder, and Henzinger, 1996]
  - Same as MTL except that the bounding interval / must be non-singular
  - $(MoveUp \land \Diamond_{[4,6]}Arrive)$
- ▶ ECA [Alur, Fix, and Henzinger, 1999, Henzinger, Raskin, and Schobbens, 1998]

- TPTL [Alur and Henzinger, 1989]
  - $\Diamond x. (MoveUp \land \Diamond y. (Arrive \land y = x + 5))$
- MTL [Koymans, 1990]
  - $(MoveUp \land \Diamond_{[5,5]}Arrive)$
- ► TA [Alur and Dill, 1994]
- TCTL [Alur, Courcoubetis, and Dill, 1990]
  - $A\Diamond(MoveUp \land A\Diamond_{[5,5]}Arrive)$
- MITL [Alur, Feder, and Henzinger, 1996]
  - Same as MTL except that the bounding interval / must be non-singular
  - $(MoveUp \land \Diamond_{[4,6]}Arrive)$
- ▶ ECA [Alur, Fix, and Henzinger, 1999, Henzinger, Raskin, and Schobbens, 1998]
- ECL [Raskin and Schobbens, 1999]
  - $(MoveUp \land \triangleright_{[5,5]}Arrive)$

In real world there is no infinite precision!

In real world there is no infinite precision!

Theorem: [Alur et al., 1996]

MITL can be translated into timed automata.

In real world there is no infinite precision!

Theorem: [Alur et al., 1996]

MITL can be translated into timed automata.

Theorem: [Alur et al., 1996]

Satisfiability and model checking for MITL are EXPSPACE-complete.

In real world there is no infinite precision!

Theorem: [Alur et al., 1996]

MITL can be translated into timed automata.

Theorem: [Alur et al., 1996]

Satisfiability and model checking for MITL are EXPSPACE-complete.

Too expensive?
# Metric Interval Temporal Logic (MITL)

In real world there is no infinite precision!

Theorem: [Alur et al., 1996]

MITL can be translated into timed automata.

Theorem: [Alur et al., 1996]

Satisfiability and model checking for MITL are EXPSPACE-complete.

Too expensive?

Theorem: [Raskin and Schobbens, 1999]

Satisfiability and model checking for ECL are PSPACE-complete.

# Metric Interval Temporal Logic (MITL)

In real world there is no infinite precision!

Theorem: [Alur et al., 1996]

MITL can be translated into timed automata.

Theorem: [Alur et al., 1996]

Satisfiability and model checking for MITL are EXPSPACE-complete.

Too expensive?

Theorem: [Raskin and Schobbens, 1999]

Satisfiability and model checking for ECL are PSPACE-complete.

### Theorem: [Wilke, 1994, Henzinger et al., 1998]

ECL with projection (i.e. outermost second-order quantification) is equally expressive as timed automata.

Started in 1995 (at Uppsala + Aalborg)

- Started in 1995 (at Uppsala + Aalborg)
- Model checking networks of timed automata against a fragment of TCTL
  - ▶ a pretty restricted fragment, but at least reachability is supported

- Started in 1995 (at Uppsala + Aalborg)
- Model checking networks of timed automata against a fragment of TCTL
  - > a pretty restricted fragment, but at least reachability is supported
- The de facto standard tool for timed automata

#### UPPAAL in a nutshell

<u>KG Larsen</u>, <u>P Pettersson</u>, <u>W Yi</u> - International journal on software tools for ..., 1997 - Springer Abstract. This paper presents the overal structure, the design criteria, and the main features of the tool box **Uppal**. It gives a detailed user guide which describes how to use the various tools of **Uppal** version 2.02 to construct abstract models of a real-time system, to simulate Cited by 2153 Related articles All 18 versions Cite Save

#### A tutorial on uppaal

G Behrmann, <u>A David, KG Larsen</u> - Formal methods for the design of real-..., 2004 - Springer Abstract This is a tutorial paper on the tool **Uppaal**. Its goal is to be a short introduction on the flavor of timed automata implemented in the tool, to present its interface, and to explain how to use the tool. The contribution of the paper is to provide reference examples and Cited by 1557 Related articles All 69 versions Cite Save

Practically non-existent. Why?

Practically non-existent. Why?

Standard construction [Alur, Feder, and Henzinger, 1996]: monolithic and notoriously complicated

Practically non-existent. Why?

- Standard construction [Alur, Feder, and Henzinger, 1996]: monolithic and notoriously complicated
- Simplified compositional constructions (notably [Maler, Nickovic, and Pnueli, 2005]): based on a less common model (timed signal transducers)

Practically non-existent. Why?

- Standard construction [Alur, Feder, and Henzinger, 1996]: monolithic and notoriously complicated
- Simplified compositional constructions (notably [Maler, Nickovic, and Pnueli, 2005]): based on a less common model (timed signal transducers)
- ► Usage of continuous semantics, different from existing tools (such as UPPAAL) built upon pointwise semantics

Practically non-existent. Why?

- Standard construction [Alur, Feder, and Henzinger, 1996]: monolithic and notoriously complicated
- Simplified compositional constructions (notably [Maler, Nickovic, and Pnueli, 2005]): based on a less common model (timed signal transducers)
- ► Usage of continuous semantics, different from existing tools (such as UPPAAL) built upon pointwise semantics

Construction for ECL ( $\equiv$  MITL<sub>0, $\infty$ </sub>) much simpler and adaptable to the pointwise semantics [Henzinger, 1998].

Still, most LTL-to-BA constructions are monolithic : difficult to modify them to incorporate time.

Practically non-existent. Why?

- Standard construction [Alur, Feder, and Henzinger, 1996]: monolithic and notoriously complicated
- Simplified compositional constructions (notably [Maler, Nickovic, and Pnueli, 2005]): based on a less common model (timed signal transducers)
- ► Usage of continuous semantics, different from existing tools (such as UPPAAL) built upon pointwise semantics

Construction for ECL ( $\equiv$  MITL<sub>0, $\infty$ </sub>) much simpler and adaptable to the pointwise semantics [Henzinger, 1998].

Still, most LTL-to-BA constructions are monolithic : difficult to modify them to incorporate time.

Other direction of research: usage of SMT solvers [Bersani, Rossi, and San Pietro, 2015, Kindermann, Junttila, and Niemelä, 2013, Woźna-Szcześniak, Szcześniak, M. Zbrzezny, and Zbrzezny, 2014]

Theorem: [Alur, Feder, and Henzinger, 1996]

MITL can be translated into continuous timed automata.

Theorem: [Alur, Feder, and Henzinger, 1996]

MITL can be translated into continuous timed automata.

Theorem: [Brihaye, Estiévenart, and Geeraerts, 2014]

MITL can be translated into *pointwise* timed automata.

Theorem: [Alur, Feder, and Henzinger, 1996]

MITL can be translated into *continuous* timed automata.

Theorem: [Brihaye, Estiévenart, and Geeraerts, 2014]

MITL can be translated into *pointwise* timed automata.

Theorem: [Alur, Feder, and Henzinger, 1996]

MITL can be translated into *continuous* timed automata.

Theorem: [Brihaye, Estiévenart, and Geeraerts, 2014]

MITL can be translated into *pointwise* timed automata.

This work:

Compositional

#### Theorem: [Alur, Feder, and Henzinger, 1996]

MITL can be translated into *continuous* timed automata.

Theorem: [Brihaye, Estiévenart, and Geeraerts, 2014]

MITL can be translated into *pointwise* timed automata.

- Compositional
- Less states (subsumes [Gastin and Oddoux, 2001])

#### Theorem: [Alur, Feder, and Henzinger, 1996]

MITL can be translated into continuous timed automata.

Theorem: [Brihaye, Estiévenart, and Geeraerts, 2014]

MITL can be translated into *pointwise* timed automata.

- Compositional
- Less states (subsumes [Gastin and Oddoux, 2001])
- Less clocks

#### Theorem: [Alur, Feder, and Henzinger, 1996]

MITL can be translated into continuous timed automata.

#### Theorem: [Brihaye, Estiévenart, and Geeraerts, 2014]

MITL can be translated into *pointwise* timed automata.

- Compositional
- Less states (subsumes [Gastin and Oddoux, 2001])
- Less clocks
- ▶ Works well with UPPAAL!

## From LTL to alternating automata [Vardi, 1998]

 $\Box$ ( $a \Rightarrow \Diamond b$ )

## From LTL to alternating automata [Vardi, 1998]

 $\Box(a \Rightarrow \Diamond b)$ 



## From LTL to alternating automata [Vardi, 1998]

 $\Box$ ( $a \Rightarrow \Diamond b$ )



A run on *aaab*:



From alternating automata to non-deterministic automata

### Theorem: [Miyano and Hayashi, 1984]

An alternating Büchi automaton with n locations can be translated into a non-deterministic Büchi automaton with  $3^n$  locations.

## From alternating automata to non-deterministic automata

#### Theorem: [Miyano and Hayashi, 1984]

An alternating Büchi automaton with n locations can be translated into a non-deterministic Büchi automaton with  $3^n$  locations.

### Theorem: [Gastin and Oddoux, 2001]

An LTL formula of size *n* can be translated into a non-deterministic Büchi automaton with  $n \times 2^n$  locations.

## From alternating automata to non-deterministic automata

#### Theorem: [Miyano and Hayashi, 1984]

An alternating Büchi automaton with n locations can be translated into a non-deterministic Büchi automaton with  $3^n$  locations.

#### Theorem: [Gastin and Oddoux, 2001]

An LTL formula of size *n* can be translated into a non-deterministic Büchi automaton with  $n \times 2^n$  locations.

The tool LTL2BA is still in wide use today.

Idea: One component automaton for each location of alternating automaton.

Idea: One component automaton for each location of alternating automaton.

A set of locations is represented by a location of the product of components, e.g.,



Component in state  $1 \Longleftrightarrow$  corresponding location in the configuration of the alternating automaton

Idea: One component automaton for each location of alternating automaton.

A set of locations is represented by a location of the product of components, e.g.,



Component in state  $1 \Longleftrightarrow$  corresponding location in the configuration of the alternating automaton

How to synchronise these components?

For each component  $C_{\varphi}$ , we add a fresh proposition  $p_{\varphi}$  (a **trigger**).

For each component  $C_{\varphi}$ , we add a fresh proposition  $p_{\varphi}$  (a **trigger**).

For each component  $C_{\varphi}$ , we add a fresh proposition  $p_{\varphi}$  (a **trigger**).



For each component  $C_{\varphi}$ , we add a fresh proposition  $p_{\varphi}$  (a **trigger**).



For each component  $C_{\varphi}$ , we add a fresh proposition  $p_{\varphi}$  (a **trigger**).



For each component  $C_{\varphi}$ , we add a fresh proposition  $p_{\varphi}$  (a **trigger**).


For each component  $C_{\varphi}$ , we add a fresh proposition  $p_{\varphi}$  (a **trigger**).

E.g.  $C_{\varphi_1 \mathcal{U} \varphi_2}$ :



For each component  $C_{\varphi}$ , we add a fresh proposition  $p_{\varphi}$  (a **trigger**).

E.g.  $C_{\varphi_1 \mathcal{U} \varphi_2}$ :



For each component  $C_{\varphi}$ , we add a fresh proposition  $p_{\varphi}$  (a **trigger**).

E.g.  $C_{\varphi_1 \mathcal{U} \varphi_2}$ :



### Proposition:

If  $\mathcal{C}_{\varphi_1 \mathcal{U} \varphi_2}$  accepts a (timed) word  $\rho$  then  $\rho \models \Box(\rho_{\varphi} \Rightarrow \varphi_1 \mathcal{U} \varphi_2)$ .

For each component  $C_{\varphi}$ , we add a fresh proposition  $p_{\varphi}$  (a **trigger**).

E.g.  $C_{\varphi_1 \mathcal{U} \varphi_2}$ :



### **Proposition**:

If  $\mathcal{C}_{\varphi_1 \mathcal{U} \varphi_2}$  accepts a (timed) word  $\rho$  then  $\rho \models \Box(p_{\varphi} \Rightarrow \varphi_1 \mathcal{U} \varphi_2)$ .

### Proposition:

For each LTL formula  $\varphi$  over AP, we can construct a Büchi automaton  $\mathcal{A}_{\varphi} = \mathcal{C}_{\psi_1} \times \cdots \times \mathcal{C}_{\psi_n}$  over AP  $\cup$  AP' such that  $\mathcal{L}(\varphi) = \mathcal{L}(\operatorname{proj}_{AP}(\mathcal{A}_{\varphi}))$ . Compositional Gastin-Oddoux: full example  $\varphi = \Box(p \Rightarrow \Diamond q) \equiv \bot \mathcal{R} (\neg p \lor \top \mathcal{U} q)$ 







Compositional Gastin-Oddoux: full example  $\varphi = \Box(p \Rightarrow \Diamond q) \equiv \bot \mathcal{R} (\neg p \lor \top \mathcal{U} q)$ 



Compositional Gastin-Oddoux: full example  $\varphi = \Box(p \Rightarrow \Diamond q) \equiv \bot \mathcal{R} (\neg p \lor \top \mathcal{U} q)$ 



 $\Box(a \Rightarrow \Diamond_{[0,2]} b)$ 

 $\Box(a \Rightarrow \Diamond_{[0,2]} b)$ 



 $\Box(a \Rightarrow \Diamond_{[0,2]} b)$ 



A run on (a, 0.5)(a, 0.6)(a, 1.2)(b, 2.3):



 $\Box(\textbf{a} \Rightarrow \Diamond_{[0,2]} \textbf{b})$ 



A run on (a, 0.5)(a, 0.6)(a, 1.2)(b, 2.3):



In this case we can simply keep the 'oldest'  $\Diamond$ .

The MITL fragment in which all intervals are of the form  $< c, \leqslant c, > c$  or  $\geqslant c.$ 

The MITL fragment in which all intervals are of the form  $< c, \le c, > c$  or  $\ge c$ .



The MITL fragment in which all intervals are of the form  $< c, \le c, > c$  or  $\ge c$ .





The MITL fragment in which all intervals are of the form  $< c, \le c, > c$  or  $\ge c$ .





The MITL fragment in which all intervals are of the form  $< c, \le c, > c$  or  $\ge c$ .



The MITL fragment in which all intervals are of the form  $< c, \le c, > c$  or  $\ge c$ .



The MITL fragment in which all intervals are of the form  $< c, \le c, > c$  or  $\ge c$ .



The MITL fragment in which all intervals are of the form  $< c, \le c, > c$  or  $\ge c$ .

 $\mathsf{E.g.,}\ \mathcal{C}_{\varphi_1\mathcal{U}_{[2,\infty]}\varphi_2}:$ 



The MITL fragment in which all intervals are of the form  $< c, \le c, > c$  or  $\ge c$ .

 $\mathsf{E.g.,}\ \mathcal{C}_{\varphi_1\mathcal{U}_{[2,\infty]}\varphi_2}:$ 



The MITL fragment in which all intervals are of the form  $< c, \le c, > c$  or  $\ge c$ .

 $\mathsf{E.g.,}\ \mathcal{C}_{\varphi_1\mathcal{U}_{[2,\infty]}\varphi_2}:$ 



### Proposition:

For each  $MITL_{0,\infty}$  formula  $\varphi$  with *n* timed subformulas, we can construct a projection-equivalent timed automaton  $A_{\varphi}$  that uses *n* clocks.

New semantics for OCATA [Brihaye, Estiévenart, and Geeraerts, 2013]:

- allows one to bound the number of clock copies
- sufficiently expressive for MITL

New semantics for OCATA [Brihaye, Estiévenart, and Geeraerts, 2013]:

- allows one to bound the number of clock copies
- sufficiently expressive for MITL

 $\varphi = \Box(a \Rightarrow \Diamond [[1, 2]]b)$ 



New semantics for OCATA [Brihaye, Estiévenart, and Geeraerts, 2013]:

- allows one to bound the number of clock copies
- sufficiently expressive for MITL

 $\varphi = \Box(a \Rightarrow \Diamond [[1, 2]]b)$   $\hline \begin{array}{c} a & a \\ \bullet & \bullet \\ 0.50.6 \\ 0.3 \end{array} \xrightarrow{b} \\ 1.2 \\ 1.3 \end{array} \xrightarrow{b} \\ 2.3 \end{array}$ 

New semantics for OCATA [Brihaye, Estiévenart, and Geeraerts, 2013]:

- allows one to bound the number of clock copies
- sufficiently expressive for MITL

 $\varphi = \Box(a \Rightarrow \Diamond [[1,2]]b)$   $\boxed{\begin{array}{c} a & a \\ \bullet & \bullet \\ 0.50.6 \\ 0.3 \end{array}} \xrightarrow{b} \\ 1.2 \\ 1.3 \end{array}$ 

To check that this timed word satisfies  $\varphi,$  we do not need to remember the exact timestamp of each a

## Example run with the interval semantics





## Example run with the interval semantics







Case 1:  $\{1, 2, 3\}$ 



Case 1: {1,2,3} Case 2: {1}, {2,3}



Case 1:  $\{1, 2, 3\}$ Case 2:  $\{1\}$ ,  $\{2, 3\}$ Case 3:  $\{1, 2\}$ ,  $\{3\}$ 



Case 1: 
$$\{1, 2, 3\}$$
  
Case 2:  $\{1\}$ ,  $\{2, 3\}$   
Case 3:  $\{1, 2\}$ ,  $\{3\}$   
Case 4:  $\{1, 2\}$ ,  $\{2, 3\}$  or  $\{1, 2, 3\}$ ,  $\{2, 3\}$ 



Case 1: 
$$\{1, 2, 3\}$$
  
Case 2:  $\{1\}$ ,  $\{2, 3\}$   
Case 3:  $\{1, 2\}$ ,  $\{3\}$   
Case 4:  $\{1, 2\}$ ,  $\{2, 3\}$  or  $\{1, 2, 3\}$ ,  $\{2, 3\}$   
In each case we only need to keep track of two clock values.





Case 1: Another branching into Case 1, Case 3 and Case 4 Case 2: Done Case 3: Done Case 4: Done



Case 1: Another branching into Case 1, Case 3 and Case 4 Case 2: Done Case 3: Done Case 4: Done

#### **Proposition:**

For each MITL formula  $\varphi = \varphi_1 \mathcal{U}_I \varphi_2$ ,  $\mathcal{C}_{\varphi}$  uses  $2 \cdot \lceil \frac{\sup I}{|I|} \rceil + 2$  clocks.

Up to half the number of clocks obtained in [Brihaye, Estiévenart, and Geeraerts, 2014]
## Experiments

We have implemented the translation in the tool  $\rm MIGHTYL.$ 

## Experiments

We have implemented the translation in the tool  $\rm MIGHTYL.$ 

$$F(k, I) = \bigwedge_{i=1}^{k} \Diamond_{i} p_{i}$$
  

$$U(k, I) = (\cdots (p_{1} \mathcal{U}_{I} p_{2}) \mathcal{U}_{I} \cdots) \mathcal{U}_{I} p_{k}$$
  

$$\theta(k, I) = \neg((\bigwedge_{i=1}^{k} \Box \Diamond p_{i}) \Rightarrow \Box(q \Rightarrow \Diamond_{I} r))$$

$$G(k, I) = \bigwedge_{i=1}^{k} \Box_{I} p_{i}$$
  

$$R(k, I) = (\cdots (p_{1} \mathcal{R}_{I} p_{2}) \mathcal{R}_{I} \cdots) \mathcal{R}_{I} p_{k}$$
  

$$\mu(k) = \bigwedge_{i=1}^{k} \Diamond_{[3(i-1),3i]} t_{i} \land \Box \neg p$$

Formula	MightyL	LTSMIN	Uppaal	Formula	MightyL	LTSMIN	Uppaal
$F(5, [0, \infty))$	9ms	3.48s/2.18s/0.12s	0.75s	$U(5, [0, \infty))$	16ms	1.90s/1.44s/0.05s	0.41s
F(5, [0, 2])	7ms	3.76s/2.23s/0.15s	0.84s	U(5, [0, 2])	8ms	2.08s/1.54s/0.06s	0.42s
$F(5, [2, \infty))$	бms	3.76s/2.26s/0.91s	1.64s	$U(5, [2, \infty))$	8ms	2.08s/1.53s/0.09s	0.52s
F(3,[1,2])	70ms	6m5.15s/38.01s/0.22s	9.00s	U(3, [1, 2])	49ms	4m0.14s/23.54s/0.09s	4.92s
F(5, [1, 2])	70ms	>15m	2m6s	U(5, [1, 2])	97ms	>15m	21.80s
$G(5, [0, \infty))$	10ms	3.83s/2.43s/0.05s	0.75s	$R(5, [0, \infty))$	7ms	1.86s/1.42s/0.03s	0.40s
G(5, [0, 2])	10ms	4.01s/2.51s/0.10s	0.82s	R(5, [0, 2])	7ms	1.97s/1.44s/0.03s	0.40s
$G(5, [2, \infty))$	9ms	4.06s/2.47s/0.04s	0.85s	$R(5, [2, \infty))$	7ms	1.92s/1.42s/0.03s	0.42s
G(5, [1, 2])	15ms	7.81s/2.99s/0.09s	1.12s	R(5, [1, 2])	10ms	5.37s/2.16s/0.04s	0.62s
$\mu(1)$	13ms	-	0.39s	$\theta(1, [100, 1000])$	9ms	1.88s/1.74s/0.04s	0.25s
μ(2)	21ms	-	2.33s	$\theta(2, [100, 1000])$	13ms	5.04s/3.17s/0.19s	0.86s
μ(3)	76ms	-	15.77s	$\theta(3, [100, 1000])$	14ms	36.57s/16.27s/3.20s	21.84s
μ(4)	87ms	-	2m23s	$\theta(4, [100, 1000])$	15ms	5m30s/4m18s/2m16s	18m39s

## Experiments

We have implemented the translation in the tool  $\rm MIGHTYL.$ 

$$F(k, I) = \bigwedge_{i=1}^{k} \Diamond_{i} p_{i}$$
  

$$U(k, I) = (\cdots (p_{1} \mathcal{U}_{I} p_{2}) \mathcal{U}_{I} \cdots) \mathcal{U}_{I} p_{k}$$
  

$$\theta(k, I) = \neg((\bigwedge_{i=1}^{k} \Box \Diamond p_{i}) \Rightarrow \Box(q \Rightarrow \Diamond_{I} r))$$

$$G(k, I) = \bigwedge_{i=1}^{k} \Box_{I} p_{i}$$
  

$$R(k, I) = (\cdots (p_{1} \mathcal{R}_{I} p_{2}) \mathcal{R}_{I} \cdots) \mathcal{R}_{I} p_{k}$$
  

$$\mu(k) = \bigwedge_{i=1}^{k} \Diamond_{[3(i-1),3i]} t_{i} \land \Box \neg p$$

Formula	MightyL	LTSMIN	Uppaal	Formula	MightyL	LTSMIN	UPPAAL
$F(5, [0, \infty))$	9ms	3.48s/2.18s/0.12s	0.75s	$U(5, [0, \infty))$	16ms	1.90s/1.44s/0.05s	0.41s
F(5, [0, 2])	7ms	3.76s/2.23s/0.15s	0.84s	U(5, [0, 2])	8ms	2.08s/1.54s/0.06s	0.42s
$F(5, [2, \infty))$	бms	3.76s/2.26s/0.91s	1.64s	$U(5, [2, \infty))$	8ms	2.08s/1.53s/0.09s	0.52s
F(3,[1,2])	70ms	6m5.15s/38.01s/0.22s	9.00s	U(3, [1, 2])	49ms	4m0.14s/23.54s/0.09s	4.92s
F(5, [1, 2])	70ms	>15m	2m6s	U(5, [1, 2])	97ms	>15m	21.80s
$G(5, [0, \infty))$	10ms	3.83s/2.43s/0.05s	0.75s	$R(5, [0, \infty))$	7ms	1.86s/1.42s/0.03s	0.40s
G(5, [0, 2])	10ms	4.01s/2.51s/0.10s	0.82s	R(5, [0, 2])	7ms	1.97s/1.44s/0.03s	0.40s
$G(5, [2, \infty))$	9ms	4.06s/2.47s/0.04s	0.85s	$R(5, [2, \infty))$	7ms	1.92s/1.42s/0.03s	0.42s
G(5, [1, 2])	15ms	7.81s/2.99s/0.09s	1.12s	R(5, [1, 2])	10ms	5.37s/2.16s/0.04s	0.62s
$\mu(1)$	13ms	-	0.39s	$\theta(1, [100, 1000])$	9ms	1.88s/1.74s/0.04s	0.25s
μ(2)	21ms	-	2.33s	$\theta(2, [100, 1000])$	13ms	5.04s/3.17s/0.19s	0.86s
μ(3)	76ms	-	15.77s	$\theta(3, [100, 1000])$	14ms	36.57s/16.27s/3.20s	21.84s
μ(4)	87ms	-	2m23s	$\theta(4, [100, 1000])$	15ms	5m30s/4m18s/2m16s	18m39s

Formula	MightyL	LTSMIN	Uppaal	SMT-based approach
$\Diamond_{[0,30]}(p \Rightarrow \Box_{[0,20]}p)$ valid	7ms	0.98s	0.32s	7s
$\Box_{[0,30]} \neg p \lor \Diamond_{[0,20]} p$ valid	7ms	0.95s	0.14s	not considered
$\Diamond_{[0,30]} p \land \Diamond_{[0,20]} p$ redundant	13ms	1.99s	0.44s	14s
$\Box_{[0,20]} \Diamond_{[0,20]} p \land \Box_{[0,40]} p \land \Diamond_{[20,40]} \top \text{ redundant}$	22ms	1m26s	2.63s	not considered

Summary for the satisfiability/model-checking of MITL

Contributions:

- A compositional translation from MITL to timed automata
- ► An implementation that works with UPPAAL and the like

Summary for the satisfiability/model-checking of MITL

Contributions:

- A compositional translation from MITL to timed automata
- ► An implementation that works with UPPAAL and the like

Possible future directions:

- Native support for ECL
- Past modalities, counting modalities
- Antichain-based optimisations

▶ ...

# Part 2: reactive synthesis

Based on a joint work with Thomas Brihaye (UMONS), Morgane Estiévenart (UMONS), Gilles Geeraerts (ULB), Hsi-Ming Ho (UMONS) and Nathalie Sznajder (LIP6, UPMC)



#### Published at FORMATS 2016 @ Quebec City



 $\boldsymbol{\Sigma} = \boldsymbol{\Sigma}_{\boldsymbol{C}} \uplus \boldsymbol{\Sigma}_{\boldsymbol{\textit{E}}}$ 

- controllable actions owned by controller C: {MoveUp, MoveDown, OpenDoor, Opened, ...}
- uncontrollable actions owned by environment E: {*0F-Up*, *0F-Down*,...,*-1F*, *0F*,... *Open*, *Close*,...}



 $\boldsymbol{\Sigma} = \boldsymbol{\Sigma}_{\boldsymbol{C}} \uplus \boldsymbol{\Sigma}_{\boldsymbol{\textit{E}}}$ 

- controllable actions owned by controller C: {MoveUp, MoveDown, OpenDoor, Opened, ...}
- uncontrollable actions owned by environment E: {0F-Up, 0F-Down, ..., -1F, 0F, ... Open, Close, ...}

+ state (at which floor, opening,  $\dots$ )



 $\boldsymbol{\Sigma} = \boldsymbol{\Sigma}_{\boldsymbol{C}} \uplus \boldsymbol{\Sigma}_{\boldsymbol{\textit{E}}}$ 

- controllable actions owned by controller C: {MoveUp, MoveDown, OpenDoor, Opened, ...}
- uncontrollable actions owned by environment E: {0F-Up, 0F-Down, ..., -1F, 0F, ... Open, Close, ...}

+ state (at which floor, opening,  $\dots$  )

+ timing restrictions (latency,  $\dots)$ 



 $\boldsymbol{\Sigma} = \boldsymbol{\Sigma}_{\boldsymbol{C}} \uplus \boldsymbol{\Sigma}_{\boldsymbol{\textit{E}}}$ 

- controllable actions owned by controller C: {MoveUp, MoveDown, OpenDoor, Opened, ...}
- uncontrollable actions owned by environment E: {0F-Up, 0F-Down, ..., -1F, 0F, ... Open, Close, ...}

+ state (at which floor, opening,  $\dots)$ 

+ timing restrictions (latency,  $\ldots)$ 

Plant  $\mathcal{P}:$  a DTA over  $\Sigma$ 



 $\boldsymbol{\Sigma} = \boldsymbol{\Sigma}_{\boldsymbol{C}} \uplus \boldsymbol{\Sigma}_{\boldsymbol{\textit{E}}}$ 

- controllable actions owned by controller C: {MoveUp, MoveDown, OpenDoor, Opened, ...}
- uncontrollable actions owned by environment E: {0F-Up, 0F-Down, ..., -1F, 0F, ... Open, Close, ...}

+ state (at which floor, opening,  $\dots)$ 

+ timing restrictions (latency,  $\ldots)$ 

Plant  $\mathcal{P}:$  a DTA over  $\Sigma$ 



A run of  $\mathcal{P}$  can be seen as a play of the *timed game* between C and E.

A run of  $\mathcal{P}$  can be seen as a play of the *timed game* between C and E. In each round, each player proposes a pair (delay, action) **enabled in**  $\mathcal{P}$ :

A run of  $\mathcal{P}$  can be seen as a play of the *timed game* between C and E. In each round, each player proposes a pair (delay, action) **enabled in**  $\mathcal{P}$ :



A run of  $\mathcal{P}$  can be seen as a play of the *timed game* between C and E. In each round, each player proposes a pair (delay, action) **enabled in**  $\mathcal{P}$ :



A run of  $\mathcal{P}$  can be seen as a play of the *timed game* between C and E. In each round, each player proposes a pair (delay, action) **enabled in**  $\mathcal{P}$ :



 $(\Delta_C, Closed) \qquad (\Delta_E, Open)$ 

Only action(s) with the shortest delay  $\min(\Delta_C, \Delta_E)$  may be played.

'The lift responds to any call in less than 10 t.u.'

'The lift responds to any call in less than 10 t.u.'



. . .

'The lift responds to any call in less than 10 t.u.'



Specification  $\mathcal{L}:$  a set of timed words over  $\Sigma$ 

'The lift responds to any call in less than 10 t.u.'



Specification  $\mathcal{L}:$  a set of timed words over  $\Sigma$ 

### **Reactive synthesis problem** (RS)

Given plant  $\mathcal{P}$  and specification  $\mathcal{L}$ , find a strategy of Controller such that no matter what Environment does, every play satisfies the specification.

'The lift responds to any call in less than 10 t.u.'



Specification  $\mathcal{L}:$  a set of timed words over  $\Sigma$ 

### **Reactive synthesis problem** (RS)

Given plant  $\mathcal{P}$  and specification  $\mathcal{L}$ , find a strategy of Controller such that no matter what Environment does, every play satisfies the specification.

Realizability problem: the special case where all actions are always enabled, i.e.,  $\mathcal{P}$  is a universal DTA over  $\Sigma$ .

## Metric Temporal Logic (MTL)

 $\varphi ::= \top \mid a \mid \neg \varphi \mid \varphi \land \varphi \mid \varphi \mathsf{U}_{I} \varphi$ with  $a \in \Sigma$ ,  $I \subseteq [0, \infty)$  with bounds in  $\mathbb{Q} \cup \{+\infty\}$ 

Models: finite (or infinite) timed words  $\sigma = (a_1, t_1)(a_2, t_2) \cdots$ 



Theorem: [Doyen, Geeraerts, Raskin, and Reichert, 2009]

Reactive synthesis problem is undecidable for ECL (hence, MTL) specifications, even without plant.

Let  $\ensuremath{\mathcal{P}}$  be a universal plant, and the spec be

'Each a is followed exactly 1 t.u. later by a b.'

Let  $\ensuremath{\mathcal{P}}$  be a universal plant, and the spec be

'Each a is followed exactly 1 t.u. later by a b.'

As an MTL formula:

$$\Box(a \implies \neg \Diamond_{>1} \top \lor \Diamond_{=1} b)$$

Let  $\ensuremath{\mathcal{P}}$  be a universal plant, and the spec be

'Each a is followed exactly 1 t.u. later by a b.'

As an MTL formula:

$$\Box(a \implies \neg \Diamond_{>1} \top \lor \Diamond_{=1} b)$$

**CONTROLLABLE** for RS: C acknowledges each *a* (in chronological order) by playing a *b* 1 t.u. after

Let  $\ensuremath{\mathcal{P}}$  be a universal plant, and the spec be

'Each a is followed exactly 1 t.u. later by a b.'

As an MTL formula:

$$\Box(a \implies \neg \Diamond_{>1} \top \lor \Diamond_{=1} b)$$

**CONTROLLABLE** for RS: C acknowledges each a (in chronological order) by playing a b 1 t.u. after

▶ C requires unbounded memory: unboundedly many *a*'s in 1 t.u.

Let  $\ensuremath{\mathcal{P}}$  be a universal plant, and the spec be

'Each a is followed exactly 1 t.u. later by a b.'

As an MTL formula:

$$\Box(a \implies \neg \Diamond_{>1} \top \lor \Diamond_{=1} b)$$

**CONTROLLABLE** for RS: C acknowledges each a (in chronological order) by playing a b 1 t.u. after

▶ C requires unbounded memory: unboundedly many *a*'s in 1 t.u.

Theorem: [Doyen, Geeraerts, Raskin, and Reichert, 2009]

The infinite-word realizability problem is undecidable for ECL specifications.

### Implementable reactive synthesis (IRS)

- $\mathsf{C} = \mathsf{deterministic} \text{ symbolic transition system } \mathcal{T}$ 
  - set of locations; if finite  $\rightarrow \mathcal{T}$  is a DTA
  - finite set of clocks X
  - finite set of possible clock constraints precision (m, K):

 $g ::= \top \mid g \land g \mid x < \alpha/m \mid x \leqslant \alpha/m \mid x = \alpha/m \mid x \geqslant \alpha/m \mid x > \alpha/m$ 

with  $x \in X$ ,  $m \in \mathbb{N}$  and  $0 \leq \alpha \leq K$ .

### Implementable reactive synthesis (IRS)

- $\mathsf{C} = \mathsf{deterministic} \text{ symbolic transition system } \mathcal{T}$ 
  - set of locations; if finite  $\rightarrow \mathcal{T}$  is a DTA
  - finite set of clocks X
  - finite set of possible clock constraints *precision* (m, K):

 $g ::= \top \mid g \land g \mid x < \alpha/m \mid x \leqslant \alpha/m \mid x = \alpha/m \mid x \geqslant \alpha/m \mid x > \alpha/m$ 

with  $x \in X$ ,  $m \in \mathbb{N}$  and  $0 \leq \alpha \leq K$ .

#### Definition

**Implementable reactive synthesis problem** (IRS): Given  $\mathcal{P}$  and  $\mathcal{L}$ , find such a  $\mathcal{T}$  that no matter what  $\mathbf{E}$  does, every play satisfies the specification.

Let  $\ensuremath{\mathcal{P}}$  be a universal plant, and the spec be

'Each a is followed exactly 1 t.u. later by a b.'

As an MTL formula:

$$\Box(a \implies \neg \Diamond_{>1} \top \lor \Diamond_{=1} b)$$

**CONTROLLABLE** for RS: C acknowledges each a (in chronological order) by playing a b 1 t.u. after

▶ C requires unbounded memory: unboundedly many *a*'s in 1 t.u.

Let  $\ensuremath{\mathcal{P}}$  be a universal plant, and the spec be

'Each a is followed exactly 1 t.u. later by a b.'

As an MTL formula:

$$\Box(a \implies \neg \Diamond_{>1} \top \lor \Diamond_{=1} b)$$

**CONTROLLABLE** for RS: C acknowledges each *a* (in chronological order)

by playing a b 1 t.u. after

▶ C requires unbounded memory: unboundedly many *a*'s in 1 t.u.

#### NOT CONTROLLABLE for IRS

• each  $\mathcal{T}$  has a bounded set of clocks

## Reactive synthesis for $\mathsf{MTL}$

Reactive synthesis	Undec.	[Doyen,	Geeraerts,	Raskin,	and Reichert,	2009]
--------------------	--------	---------	------------	---------	---------------	-------

## Reactive synthesis for MTL

	Reactive synthesis	Undec. [Doyen, Geeraerts, Raskin, and Reichert, 20				
			Controller =	timed automaton		
Im	plementable reactive sy	nthesis l	Jndec. [Bouyer,	Bozzelli, and Chevalier, 20	06]	

## Recovering decidability...

Clock constraints in T:

 $g ::= \top \mid g \land g \mid x < \alpha/m \mid x \leqslant \alpha/m \mid x = \alpha/m \mid x \geqslant \alpha/m \mid x > \alpha/m$ 

with  $x \in X$ ,  $m \in \mathbb{N}$  and  $0 \leq \alpha \leq K$ .

## Recovering decidability...

Clock constraints in T:

 $g ::= \top \mid g \land g \mid x < \alpha/m \mid x \leq \alpha/m \mid x = \alpha/m \mid x \geq \alpha/m \mid x > \alpha/m$ 

with  $x \in X$ ,  $m \in \mathbb{N}$  and  $0 \leq \alpha \leq K$ .

Fix X and  $(m, K) \Longrightarrow$  the alphabet of  $\mathcal{T}$  is given!

## Recovering decidability...

Clock constraints in  $\mathcal{T}$ :

 $g ::= \top \mid g \land g \mid x < \alpha/m \mid x \leqslant \alpha/m \mid x = \alpha/m \mid x \geqslant \alpha/m \mid x > \alpha/m$ 

with  $x \in X$ ,  $m \in \mathbb{N}$  and  $0 \leq \alpha \leq K$ .

Fix X and  $(m, K) \Longrightarrow$  the alphabet of T is given!

#### Definition

**Bounded-resources reactive synthesis problem** (BResRS): *Given*  $\mathcal{P}$ ,  $\mathcal{L}$ , and a set of clocks X and precision (m, K), find such a resource-bounded  $\mathcal{T}$  that no matter what E does, every play satisfies the specification.
	Reactive synthesis	Undec. [Doyen, Geeraerts, Raskin, and Reichert, 2009]				
		Controller = timed automaton				
I	mplementable reactive sy	nthesis l	Indec. [Bouyer, Bozzelli, and Chevalier, 2006]			







# Regaining hope? Less expressive specifications

Undecidability proofs heavily use 'punctuality' of MTL:  $\underset{request}{request} \rightarrow \Diamond_{=1} \underset{grant}{grant}$ 

 $\begin{array}{l} \textit{request} \rightarrow \Diamond_{[1,2]}\textit{grant} \\ \textit{request} \rightarrow \Diamond_{\leqslant 3}\textit{grant} \end{array}$ 

MITL = non-punctual fragment of MTL:

$$\varphi ::= \top \mid \mathbf{a} \mid \neg \varphi \mid \varphi \land \varphi \mid \varphi \mathsf{U}_{\mathsf{I}} \varphi$$

with  $a \in \Sigma$ ,  $I \subseteq [0, \infty)$  is a **non-singular** with bounds in  $\mathbb{Q} \cup \{+\infty\}$ 













## From MTL to OCATA





### From MTL to OCATA



Execution on the timed word (a, 0.5)(a, 0.6)(a, 1.2)(b, 2.3):



- Action (a, g, R)
  - a: an action in  $\Sigma_C \cup \Sigma_C$
  - ▶ g: guard over clocks of X and X<sub>P</sub>
  - R: resets of clocks of X



- ► Action (*a*, *g*, *R*)
  - a: an action in  $\Sigma_C \cup \Sigma_C$
  - g: guard over clocks of X and  $X_{\mathcal{P}}$
  - R: resets of clocks of X



▶ Finite abstraction is a (time-abstract) bisimulation

- Action (a, g, R)
  - a: an action in  $\Sigma_C \cup \Sigma_C$
  - ▶ g: guard over clocks of X and X<sub>P</sub>
  - R: resets of clocks of X



- Finite abstraction is a (time-abstract) bisimulation
- Sufficient to detect when a bad configuration has been reached: one *H<sub>i</sub>* contains only accepting locations of the OCATA *A* (≡ ¬φ)

- ► Action (*a*, *g*, *R*)
  - a: an action in  $\Sigma_C \cup \Sigma_C$
  - ▶ g: guard over clocks of X and X<sub>P</sub>
  - R: resets of clocks of X



- Finite abstraction is a (time-abstract) bisimulation
- Sufficient to detect when a bad configuration has been reached: one H<sub>i</sub> contains only accepting locations of the OCATA A (≡ ¬φ)
- If tree finite and winning strategy: we have a (finite) controller  ${\mathcal T}$







For MTL specifications [Bouyer, Bozzelli, and Chevalier, 2006]: stop the computation with a well-quasi order  $\sqsubseteq$  on the labels of the nodes



Correctness: this finite tree is sufficient to answer the problem



- Correctness: this finite tree is **sufficient** to answer the problem
- Complexity: non-primitive recursive due to well-quasi orderings

The tree is finite by using interval semantics for OCATA [Brihaye, Estiévenart, and Geeraerts, 2013]: triply-exponential size

- The tree is finite by using interval semantics for OCATA [Brihaye, Estiévenart, and Geeraerts, 2013]: triply-exponential size
- ► We obtain the same complexity as [D'Souza and Madhusudan, 2002], but with an on-the-fly exploration: may terminate more quickly

- The tree is finite by using interval semantics for OCATA [Brihaye, Estiévenart, and Geeraerts, 2013]: triply-exponential size
- ▶ We obtain the same complexity as [D'Souza and Madhusudan, 2002], but with an on-the-fly exploration: may terminate more quickly
- Experimental results on a scheduling problem

		Realisat	ne instances			
T	n					
1	1	0	46 / 52	Т	п	#
1	1	1	199 / 147	2	1	
1	1	2	4,599 / 1,343	2	1	
2	2	1	2,632 / 645	2	1	
2	2	2	18,453 / 2,358	3	2	
3	3	1	182,524 / 2,297	3	2	
3	3	2	>5min	4	3	
4	4	0	54,893 / 667	4	3	
4	4	1	>5min			

	Unealisable instances							
Т	п	# clocks	exec. time (sec) / #nodes					
2	1	1 0 77 / 84						
2	1	1	824 / 311					
2	1	1 2 3,079 / 1,116 2 1 17,134 / 1698						
3	2							
3	2	2	>5min					
4	3	0	10,621 / 540					
4	3	1	>5min					

- The tree is finite by using interval semantics for OCATA [Brihaye, Estiévenart, and Geeraerts, 2013]: triply-exponential size
- ► We obtain the same complexity as [D'Souza and Madhusudan, 2002], but with an on-the-fly exploration: may terminate more quickly
- Experimental results on a scheduling problem

		Realisab	le instances						
Т	n	# clocks	exec. time (sec) / #nodes	Unealisable instance					
1	1	0	46 / 52	1	Т	п	# clocks	exec. tim	
1	1	1	199 / 147		2	1	0		
1	1	2	4,599 / 1,343		2	1	1	8	
2	2	1	2,632 / 645		2	1	2	3,0	
2	2	2	18,453 / 2,358		3	2	1	17,	
3	3	1	182,524 / 2,297		3	2	2		
3	3	2	>5min		4	3	0	10	
4	4	0	54,893 / 667		4	3	1		
4	4	1	>5min						

Can handle small but non-trivial examples: but do not scale well

exec. time (sec) / #nodes 77 / 84

> 824 / 311 3.079 / 1.116

17,134 / 1698

>5min

10,621 / 540

>5min

- The tree is finite by using interval semantics for OCATA [Brihaye, Estiévenart, and Geeraerts, 2013]: triply-exponential size
- ► We obtain the same complexity as [D'Souza and Madhusudan, 2002], but with an on-the-fly exploration: may terminate more quickly
- Experimental results on a scheduling problem

Realisable instances								
Т	T n # clocks exec. time (sec) / #nodes							
1	1 0 46 / 52							
1	1 1 1 199 / 147 1 1 2 4,599 / 1,343							
1								
2	2	2 1 2,632 / 645						
2	2 2 18,453 / 2,358							
3	3	1	182,524 / 2,297					
3	3 3 2 >5min							
4	4 4 0 54,893 / 667							
4	4 4 1 >5min							

	Unealisable instances							
Т	T n # clocks exec. time (sec) / #nodes							
2	1	0	77 / 84					
2	1	1	824 / 311					
2	1	2	3,079 / 1,116					
3	2	1	17,134 / 1698					
3	3 2 2 >5min							
4	3	0	10,621 / 540					
4	3	1	>5min					

- Can handle small but non-trivial examples: but do not scale well
- ▶ This was before MIGHTYL, which could make things easier...

For almost all reactive synthesis problems, MITL is as hard as MTL...

For almost all reactive synthesis problems, MITL is as hard as MTL...

- ... except for resources-bounded problem over finite words:
  - Non-elementary for MTL;
  - 3-EXPTIME for MITL;
  - on-the-fly algorithm

For almost all reactive synthesis problems, MITL is as hard as MTL...

- ... except for resources-bounded problem over finite words:
  - Non-elementary for MTL;
  - ► 3-EXPTIME for MITL;
  - on-the-fly algorithm

#### Other fragments?? Hopeless!

	Safety-MTL	coFlat-MTL	Open-MITL	Closed-MITL
implementable RS				
clock-bounded RS				
precision-bounded RS				

For almost all reactive synthesis problems, MITL is as hard as MTL...

- ... except for resources-bounded problem over finite words:
  - Non-elementary for MTL;
  - ► 3-EXPTIME for MITL;
  - on-the-fly algorithm

#### Other fragments?? Hopeless!

	Safety-MTL	coFlat-MTL	Open-MITL	Closed-MITL
implementable RS	undec.	undec.	undec.	undec.
clock-bounded RS	undec.	undec.	undec.	undec.
precision-bounded RS	undec.	undec.	undec.	undec.

For almost all reactive synthesis problems, MITL is as hard as MTL...

- ... except for resources-bounded problem over finite words:
  - Non-elementary for MTL;
  - 3-EXPTIME for MITL;
  - on-the-fly algorithm

#### Other fragments?? Hopeless!

	Safety-MTL	coFlat-MTL	Open-MITL	Closed-MITL
implementable RS	undec.	undec.	undec.	undec.
clock-bounded RS	undec.	undec.	undec.	undec.
precision-bounded RS	undec.	undec.	undec.	undec.

Possible future directions:

- Decidable fragments for BPrecRS/BClockRS
- Heuristics for speed-up for the on-the-fly algorithm: well-quasi orderings as in [Bouyer, Bozzelli, and Chevalier, 2006], zone-based versions?
- Experiments of the on-the-fly algorithm over the fragments
- Robustness of controllers

For almost all reactive synthesis problems, MITL is as hard as MTL...

- ... except for resources-bounded problem over finite words:
  - Non-elementary for MTL;
  - 3-EXPTIME for MITL;
  - on-the-fly algorithm

#### Other fragments?? Hopeless!

	Safety-MTL	coFlat-MTL	Open-MITL	Closed-MITL
implementable RS	undec.	undec.	undec.	undec.
clock-bounded RS	undec.	undec.	undec.	undec.
precision-bounded RS	undec.	undec.	undec.	undec.

Possible future directions:

- Decidable fragments for BPrecRS/BClockRS
- Heuristics for speed-up for the on-the-fly algorithm: well-quasi orderings as in [Bouyer, Bozzelli, and Chevalier, 2006], zone-based versions?
- Experiments of the on-the-fly algorithm over the fragments
- Robustness of controllers

# Thank you for your attention! Questions?

### References I

- Rajeev Alur and David L. Dill. A theory of timed automata. *Theoretical Computer Science*, 126 (2):183–235, 1994.
- Rajeev Alur and Thomas A. Henzinger. A really temporal logic. In 30th Annual Symposium on Foundations of Computer Science (FOCS'89), pages 164–169. IEEE Computer Society Press, 1989. doi: 10.1109/SFCS.1989.63473.
- Rajeev Alur, Costas A. Courcoubetis, and David L. Dill. Model-checking for real-time systems. In Proceedings of the Fifth Annual Symposium on Logic in Computer Science (LICS '90), pages 414–425. IEEE Computer Society Press, 1990. doi: 10.1109/LICS.1990.113766.
- Rajeev Alur, Tomás Feder, and Thomas A. Henzinger. The benefits of relaxing punctuality. *Journal of the ACM*, 43(1):116–146, 1996.
- Rajeev Alur, Limor Fix, and Thomas A. Henzinger. Event-clock automata: A determinizable class of timed automata. *Theoretical Computer Science*, 211(1-2):253–273, 1999.
- Marcello M. Bersani, Matteo Rossi, and Pierluigi San Pietro. An SMT-based approach to satisfiability checking of MITL. *Information and Computation*, 245:72–97, 2015.
- Patricia Bouyer, Laura Bozzelli, and Fabrice Chevalier. Controller synthesis for MTL specifications. In Proceedings of the 17th International Conference on Concurrency Theory (CONCUR'06), volume 4137 of Lecture Notes in Computer Science, pages 450–464. Springer, 2006.
- Thomas Brihaye, Morgane Estiévenart, and Gilles Geeraerts. On MITL and alternating timed automata. In Proceedings of the 11th international conference on Formal Modeling and Analysis of Timed Systems (FORMATS'13), volume 8053 of Lecture Notes in Computer Science, pages 47–61. Springer, 2013.
- Thomas Brihaye, Morgane Estiévenart, and Gilles Geeraerts. On MITL and alternating timed automata of infinite words. In Proceedings of the 12th International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS'14), volume 8711 of Lecture Notes in Computer Science. Springer, 2014.

## References II

- Laurent Doyen, Gilles Geeraerts, Jean-François Raskin, and Julien Reichert. Realizability of real-time logics. In Proceedings of the 7th International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS'09), volume 5813 of Lecture Notes in Computer Science, pages 133–148. Springer, 2009.
- Deepak D'Souza and P. Madhusudan. Timed control synthesis for external specifications. In Proceedings of the 19th Annual conference on Theoretical Aspects of Computer Science (STACS'02), volume 2285 of Lecture Notes in Computer Science, pages 571–582. Springer, 2002.
- Paul Gastin and Denis Oddoux. Fast LTL to Büchi automata translation. In Proceedings of the 13th International Conference on Computer Aided Verification (CAV'01), volume 2102 of Lecture Notes in Computer Science, pages 53–65. Springer, 2001.
- Thomas A. Henzinger. It's about time: Real-time logics reviewed. In Proceedings of the 9th International Conference on Concurrency Theory (CONCUR '98), volume 1466 of Lecture Notes in Computer Science, pages 439–454. Springer, 1998. doi: 10.1007/BFb0055640.
- Thomas A. Henzinger, Jean-François Raskin, and Pierre-Yves Schobbens. The regular real-time languages. In Proceedings of the 25th International Colloquium on Automata, Languages and Programming (ICALP'98), volume 1443 of Lecture Notes in Computer Science, pages 580–591. Springer, 1998. doi: 10.1007/BFb0055086.
- Roland Kindermann, Tommi A. Junttila, and Ilkka Niemelä. Bounded model checking of an MITL fragment for timed automata. In *Proceedings of the 13th International Conference on Application of Concurrency to System Design (ACSD'13)*, pages 216–225. IEEE Computer Society Press, 2013. doi: 10.1109/ACSD.2013.25.
- Ron Koymans. Specifying real-time properties with metric temporal logic. *Real-Time Systems*, 2 (4):255–299, 1990.

### References III

- Oded Maler, Dejan Nickovic, and Amir Pnueli. Real time temporal logic: Past, present, future. In Proceedings of the Third International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS'05), volume 3829 of Lecture Notes in Computer Science, pages 2–16. Springer, 2005.
- Satoru Miyano and Takeshi Hayashi. Alternating finite automata on omega-words. *Theoretical Computer Science*, 32:321–330, 1984. doi: 10.1016/0304-3975(84)90049-5.
- Joël Ouaknine and James Worrell. On the decidability of metric temporal logic. In Proceedings of the 20th Annual Symposium on Logic in Computer Science (LICS'05), pages 188–197. IEEE Computer Society Press, 2005.
- Joël Ouaknine and James Worrell. Safety metric temporal logic is fully decidable. In Proceedings of the 12th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS'06), volume 3920 of Lecture Notes in Computer Science, pages 411–425. Springer, 2006.
- Jean-François Raskin and Pierre-Yves Schobbens. The logic of event clocks: Decidability, complexity and expressiveness. *Journal of Automata, Languages and Combinatorics*, 4(3): 247–282, 1999.
- Moshe Y. Vardi. Reasoning about the past with two-way automata. In Kim G. Larsen, Sven Skyum, and Glynn Winskel, editors, Automata, Languages and Programming, volume 1443 of Lecture Notes in Computer Science, pages 628–641, 1998.
- Thomas Wilke. Specifying timed state sequences in powerful decidable logics and timed automata. In Formal Techniques in Real-Time and Fault-Tolerant Systems, volume 863 of Lecture Notes in Computer Science, pages 694–715. Springer, 1994.
- Bożena Woźna-Szcześniak, Ireneusz Szcześniak, Agnieszka M. Zbrzezny, and Andrzej Zbrzezny. Bounded model checking for weighted interpreted systems and for flat weighted epistemic computation tree logic. In Proceedings of the 17th International Conference on Principles and Practice of Multi-Agent Systems (PRIMA'14), volume 8861 of Lecture Notes in Artificial Intelligence, pages 107–115. Springer, 2014.